

SiLC Meeting in Torino, Dec 17-19, 2007

This meeting relies on an active participation of all the people attending it: discussions and questions are mostly welcome! The year 2007 was quite important for the project. The year 2008 will have even more demanding and ambitious goals wrt ILC roadmap, EUDET deliverables & development of synergies with other projects

Welcome: Monday 17, 9h30 to 10h		
Session 1: Monday 17, 10h to 13h <i>R&D on Mechanics Prototypes and starting of the sensor R&D session</i> chaired by Diego Gamba (Torino U.)		
10h15-10h30	Welcome from the Lab director	
10h30-11h05	Construction of Si Modules: the CMS experience	L. Demaria (Torino)
11h05-11h25	The LCTPC + Silicon Envelop prototype	S. Haensel (HEPHY)
11h25-11h45	<i>Coffee break</i> Next session chaired by A. Ruiz (IFCA)	
11h45-12h10	Thermal and mechanical issues in the design of thin CMOS MAPS module for the vertex detector of the Super B factory	F. Bosi (INFN-Pisa)
12h10-12h30	Alignment status report	I. Vila (IFCA/CSIC)
12h30-12h50	The 4-layers Prototype and related integration issues	G. Daubard/A.Savoy-Navarro (LPNHE)
12h50-13h10	Silicon Strip Sensor R&D and results from HPK sensor measurements	Th Bergauer (HEPHY)
13h10-14h40	<i>Lunch break</i>	
Session 2: Monday 17, 14h30 to 18h <i>R&D on Sensors (con'td)</i> , chaired by C. Da Via <i>(a large part of this section is dedicated to the novel technology R&D's to which SiLC is linked through the collaboration of SiLC members: DEPFET (IFIC + CUPrague), MAPS (U.B.), 3D (CNM, VTT and a few other institutes interested in).</i>		
14h40-15h	Sensor design for dual metal layer readout	M. Dragicevic (HEPHY)
15h-15h30	<i>130nm CMOS MAPS for the vertex detector of the SuperB factory</i>	V. de Re (Bergamo)
15h30-16h15	<i>Synergy with SuperB Factory (introduction by Giuliana Rizzo; with participation of T. Tsuboyama, KEK)</i>	Discussion conducted by G. Rizzo (Pisa)
16h15-16h35	<i>Coffee break</i> Next session on sensors (cont'd) chaired by D. Gamba (Torino)	
16h35-17h15	<i>Development of 3D detectors at FBK-irst</i>	G.F. Dalla Betta (IRST)
17h15-18h	<i>3D pixel detectors technology review</i>	C. Da Via (Manchester U.)
18h-18h30	Si sensor developments at CNM	M. Lozano (CNM-CSIC)
18h30-19h30	<i>SiLC Executive meeting (reported to January 7th)</i>	
Session 3: Tuesday 18, 9h to 12h30 <i>R&D on Sensors (cont'd) and Electronics:</i> chaired by M. Lozano(CNM)		
9h-9h30	Optical simulation of microstrip sensors	M. Fernandez (IFCA/CSIC)
9h30h-10h	Pixel R&D at IFIC-Valencia	M. Vos (IFIC/CSIC)
10h-10h30	Low material budget pixel detector with high intrinsic gain	V. Saveliev (OSU)
10h30-11h	Pixel Developments at Barcelona University	D. Gascon (B.U.)
11h-11h30	<i>Coffee break</i> Next session on Electronics chaired by M. Krammer (HEPHY)	
11h30-12h10	SiTr_130-128 present status and prospects –	H.P. Pham and J.F. Genat

	Part2(analog)	(LPNHE)
12h10-12h40	SiTr_130-128 present status and prospects – Part2(digital)	A. Comerma (Barcelona U.)
12h40-13h20	Reference electronics: VA1 vs APV25	J. David(LPNHE)/ Th. Bergauer (HEPHY)
13h20-14h40	<i>Lunch break</i>	
Session 4: Tuesday 18, 14h30 to 18h45		
<i>Electronics (end), Tests bench and Beam tests</i> chaired by Toru Tsuboyama (KEK)		
14h30-15h10	Technologies and Tools for µelectronics: What CERN can offer	A. Marchioro (CERN)
15h10-15h40	The beam test at CERN in October 2007	J. David (LPNHE)/M. Fernandez(IFCA)
15h40-16h10	CERN test beam results	W. Dasilva (LPNHE), P. Kvasnicka (CU Prague)
16h10-16h30	New DAQ for beam tests (hard & software)	J. Sanchez-Gonzalez(IFCA)
16h30-16h50	<i>Coffee break</i> Next session chaired by Valeri Saveliev (OSU)	
16h50-17h20	The LSTFE Front end status	B Schumm (SCIPP&UCSC)
17h20-18h05	<i>Test facilities at FNAL; Rreview of FNAL Silicon activities for ILC</i>	M .Demarteau (FNAL)
18h05-18h45	<i>The 4th Concept</i>	G.P. Yeh (FNAL)
<i>Meeting Dinner in Torino</i>		
Session 5: Wednesday 19, 9h to 10h30		
<i>Simulations</i> chaired by Meinhard Regler (HEPHY)		
9-9h40	<i>The ILC simulation general framework: ILCROOT</i>	C. Gatto (Lecce U.)
9h40-10h10	<i>The ILC simulation General framework: MOKKA-MarlinReco</i>	F. Gaede (DESY)
10h10-10h40	Highlights of the Simulation parallel session	Marcel Vos (IFIC/CSIC)
10h40-11h	<i>Coffee break</i>	
Session 6: Wednesday 19, 10h50 to 12h50		
<i>SiLC synergies wrt (S)LHC & other new projects</i> chaired by Franco Bedeschi (Pisa)		
11h-11h30	The test beams for 2008 and later and related issues; the SiLC roadmap	A. Savoy-Navarro (LPNHE)
11h30-12h	<i>The Silicon tracker developments at KEK</i>	T. Tsuboyoma (KEK)
12h-12h30	<i>Silicon Pixel Tracker for the ILC</i>	K. Stefanov, C. Damerell (RAL)
12h30-14h	<i>Lunch break</i>	
Session 7: Wednesday 19, 14h30 to 16h		

<i>The ILC Project: Detector Projects (cont'd), roadmap and Physics benchmarks:</i> chaired by F. Kapusta (LPNHE)		
14h-14h40	<i>The ILD project present status for LOI'08</i>	T. Behnke (DESY)
14h40-15h10	<i>The Physics Benchmarks for ILC</i>	T. Barklow (SLAC)
15h40-16h10	<i>Concluding remarks and final discussion</i>	

N.B.: In italics are the talks that will be given by invited external people.

Furthermore a parallel session on simulations is organized by Marcel and Valeri with the present agenda:

14h-14h30	Digitization <i>Z. Drasal</i>
14h30-15h	LiC detector toy <i>M. Regler</i>
15h-15h30	Additional presentation and discussion with MOKKA (Frank) and ILCROOT(Corrado)
15h30-16h	Status MOKKA <i>Saveliev</i>
16h10-16h30	Coffee break
16h30-16h50	Forward tracking <i>Marcel Vos</i>
16h50-17h10	St. Cruz simulation studies <i>B. Schumm</i> (VRVS)
17h10-18h10	Discussion